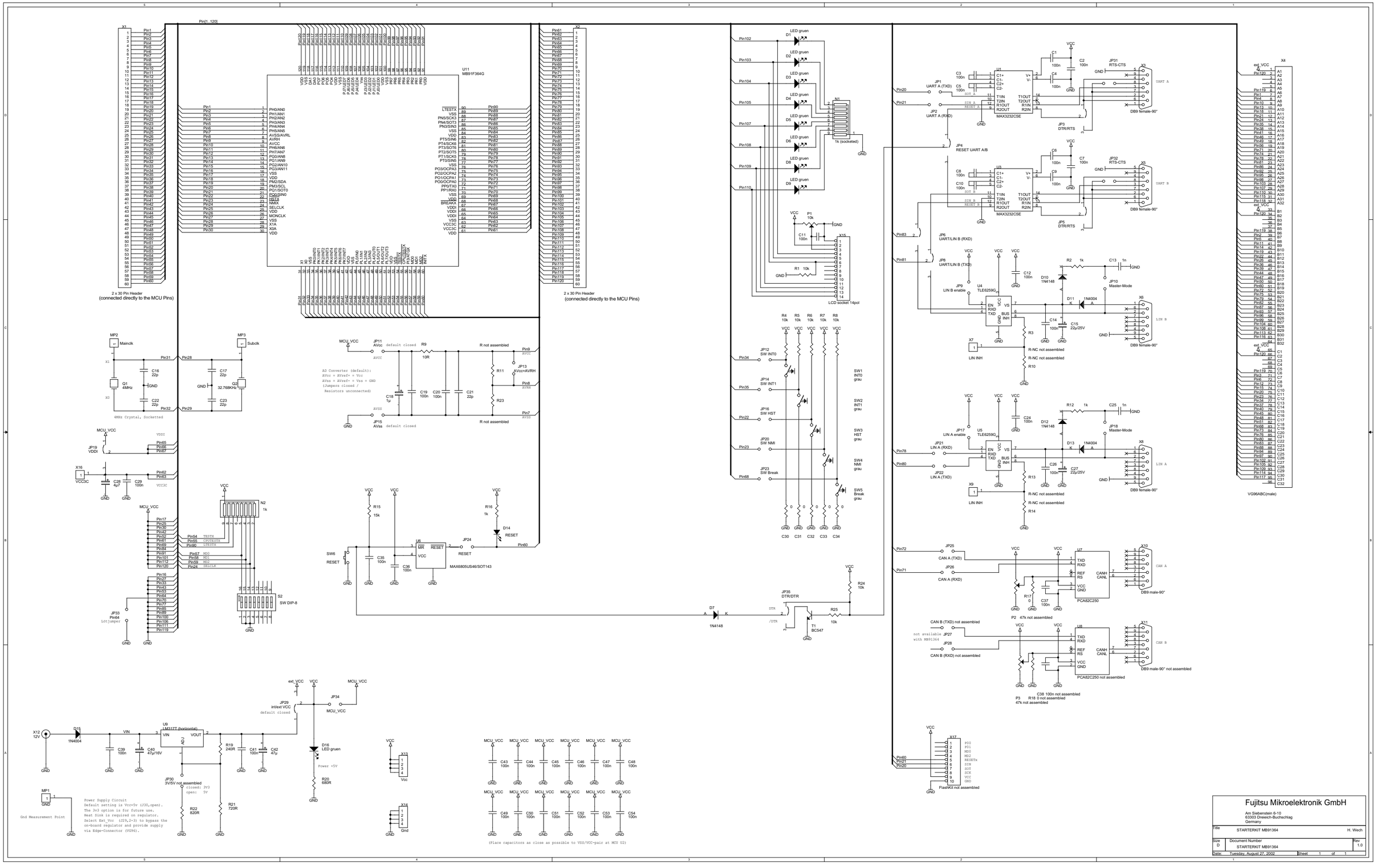
U11
MB91F364G
X1
2 x 30 Pin Header
(connected directly to the MCU Pins)
X2
2 x 30 Pin Header
(connected directly to the MCU Pins)
X4
VG96ABC(male)
LCD socket 14pol
N1
1k (socketed)
P1 10k
C11 100n
R1 10k
LED gruen
R4 10k
R5 10k
R6 10k
R7 10k
R8 10k
SW1 INT0 grau
SW2 INT1 grau
SW3 HST grau
SW4 NMI grau
SW5 Break grau
JP12 SW INT0
JP14 SW INT1
JP16 SW HST
JP20 SW NMI
JP23 SW Break
MP2 Mainclk
MP3 Subclk
Q1 4MHz
Q2 32.768KHz
C16 22p
C22 22p
C17 22p
C23 22p
4MHz Crystal, Socketted
MCU_VCC
JP11 AVcc default closed
JP15 AVss default closed
R9 10R
R11
R23
JP13 AVcc=AVRH
C18 1µ
C19 100n
C20 100n
C21 22p
AD Converter (default):
AVcc = AVref+ = Vcc
AVss = AVref- = Vss = GND
(Jumpers closed /
Resistors unconnected)
R not assembled
JP19 VDDI
X16 VCC3C
C28 4µ7
C29 100n
N2 1k
S2 SW DIP-8
JP33 Lotjumper
R15 15k
R16 1k
D14 RESET
SW6 RESET
C35 100n
C36 100n
U5 MAX6805US46/SOT143
JP24 RESET
D7 1N4148
JP35 DTR/DTR
T1 BC547
R24 10k
R25 10k
JP1 UART A (TXD)
JP2 UART A (RXD)
JP4 RESET UART A/B
JP6 UART/LIN B (RXD)
JP8 UART/LIN B (TXD)
JP9 LIN B enable
JP17 LIN A enable
JP21 LIN A (RXD)
JP22 LIN A (TXD)
U1 MAX3232CSE
U3 MAX3232CSE
U4 TLE6259G
U5 TLE6259G
JP31 RTS-CTS
JP32 RTS-CTS
JP3 DTR/RTS
JP5 DTR/RTS
X5 DB9 female-90°
UART A
UART B
JP10 Master-Mode
JP18 Master-Mode
X6 DB9 female-90°
LIN B
X7 LIN INH
X8 DB9 female-90°
LIN A
X9 LIN INH
R-NC not assembled
JP25 CAN A (TXD)
JP26 CAN A (RXD)
U7 PCA82C250
X10 DB9 male-90°
CAN A
P2 47k not assembled
CAN B (TXD) not assembled
JP27
not available with MB91364
JP28 CAN B (RXD) not assembled
U8 PCA82C250 not assembled
X11 DB9 male-90° not assembled
CAN B
C38 100n not assembled
P3 47k not assembled
R18 0 not assembled
X17 FlashKit not assembled
X12 12V
D15 1N4004
U9 LM317T (horizontal)
C39 100n
C40 47µ/16V
C41 100n
C42 47µ
R19 240R
R20 680R
R21 720R
R22 820R
D16 LED gruen
Power +5V
JP29 int/ext VCC default closed
JP34 MCU_VCC
JP30 3V/5V not assembled closed: 3V3 open: 5V
MP1 Gnd Measurement Point
Power Supply Circuit
Default setting is Vcc=5v (J30,open).
The 3v3 option is for future use.
Heat Sink is required on regulator.
Select Ext_Vcc (J29,2-3) to bypass the
on-board regulator and provide supply
via Edge-Connector (VG96).
X13 Vcc
X14 Gnd
C43 100n
C44 100n
C45 100n
C46 100n
C47 100n
C48 100n
C49 100n
C50 100n
C51 100n
C52 100n
C53 100n
C54 100n
(Place capacitors as close as possible to VSS/VCC-pair at MCU U2)
Fujitsu Mikroelektronik GmbH
Am Siebenstein 6-10
63303 Dreieich-Buchschlag
Germany
Title STARTERKIT MB91364
H. Wech
Size D
Document Number STARTERKIT MB91364
Rev 1.0
Date: Tuesday, August 27, 2002
Sheet 1 of 1