

DSU-FR EMULATOR

PGA-179P ADAPTER

MB2198-100

OPERATION MANUAL

PREFACE

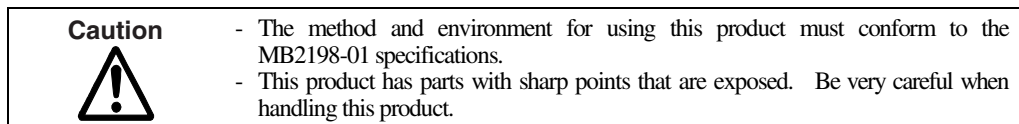
Thank you for purchasing the PGA-179P adapter (model type: MB2198-100) for the DSU-FR emulator. This adapter is used in the adapter unit to connect a user system using the DSU-FR(*1) emulator (MB2198-01 and MB2198-10) (herein called the emulator) and MB91301 (LQFP-144P). This manual explains how to use the PGA-179P adapter with the DSU-FR emulator. Before using this product, be sure to read and understand this manual.

*1: FR is the abbreviation used for FUJITSU RISC CONTROLLER, which is a Fujitsu product.

■ Handling this product

All information about how to handle this product and the required precautions for using this product safely is given in the DSU-FR emulator hardware manual.

To use this product, follow the instructions shown in the DSU-FR Emulator MB2198-01 Hardware Manual.



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1. Checking the Delivered Product and Accessories

Before using this product, confirm that all the parts listed below were received.

- PGA-179P adapter board: 1
- Operation manual (in Japanese): 1
- Operation manual (in English, this manual): 1

This product functions as an adapter unit when it is combined with the header board.

2. Handling Precautions

The adapter unit is carefully designed to provide reliable contact between the parts by using a sophisticated structure and precise dimensions. Because of this, the adapter unit is less sturdy than conventional products of this kind. Always use the adapter unit correctly in the prescribed environment. Note the following points regarding the installation and removal of the adapter board.

- During connection with the adapter unit, do not apply any stress to the NQPACK mounted in the user system.

■ Connecting the header board

- To connect the header board to the adapter board, match the position of the adapter I/F connector mounted on the header board with that of the header I/F connector mounted on the adapter board, and then insert the adapter board until it clicks into place. (See Figure 2-1.)
- To disconnect the adapter board from the header board, pull out the adapter board while holding the header board so that no stress is applied to the NQPACK mounted on the user system.

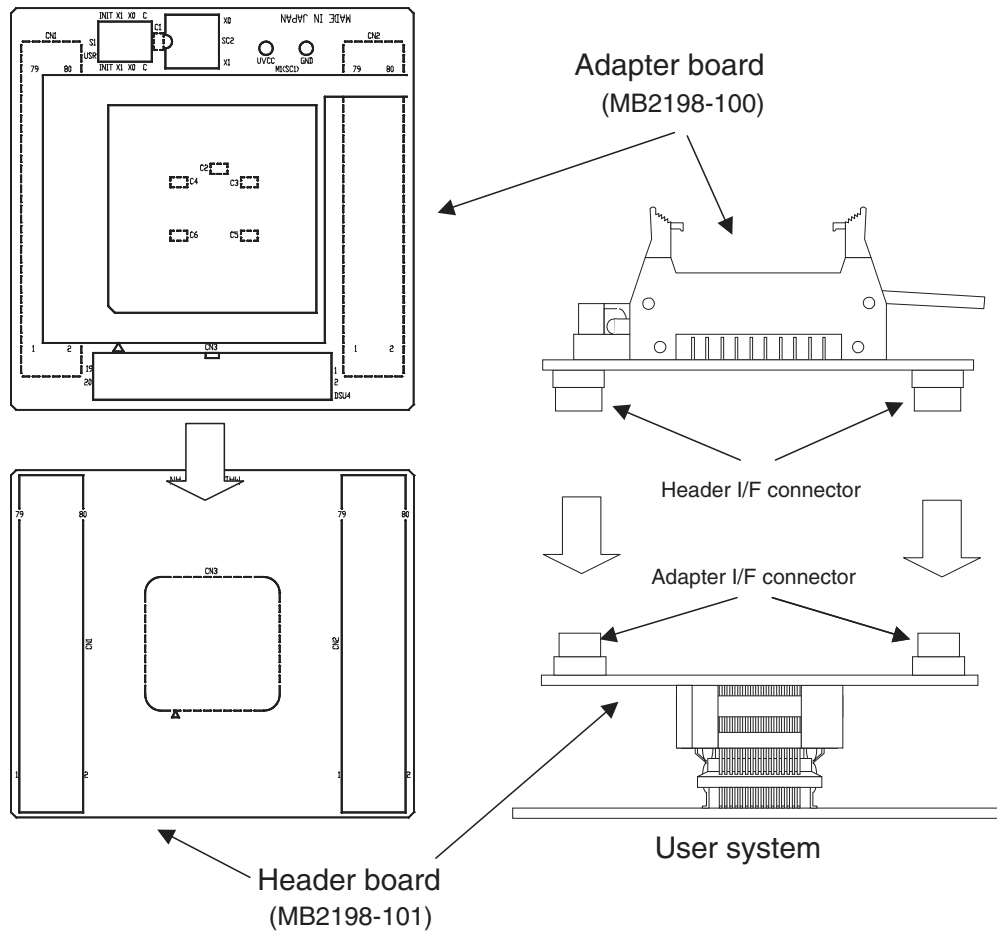


Figure 2-1 Adapter board connection

■ Connecting the DSU-FR cable

To connect the DSU-FR cable to the adapter board, open the latches on either side of the connector, insert the DSU-FR cable connector, and then close the latches. (See Figure 2-2.)

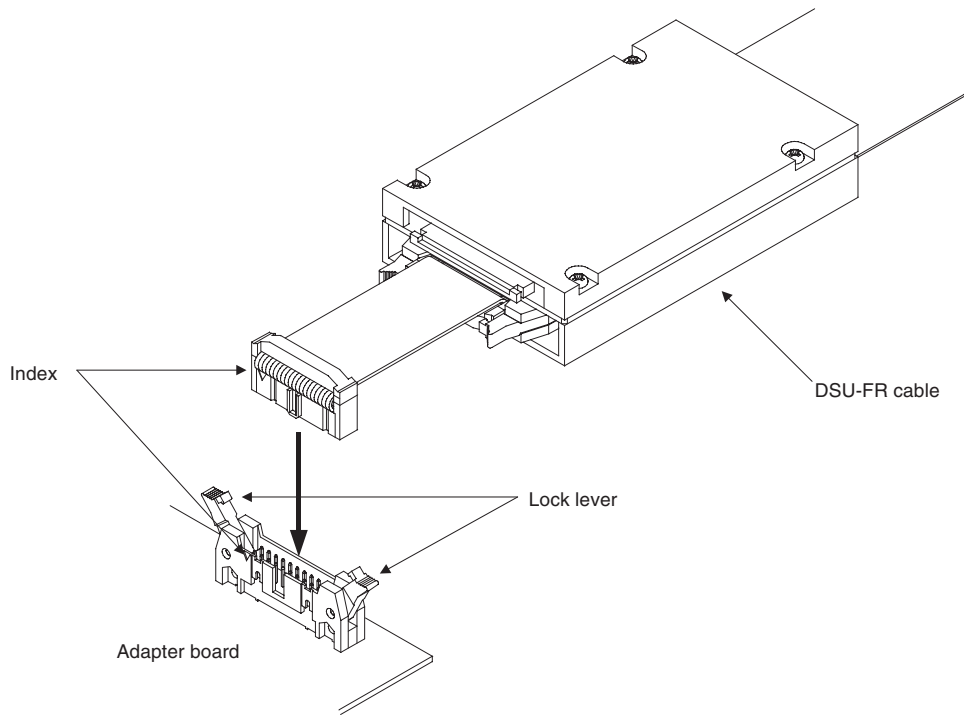


Figure 2-2 DSU-FR cable connection

3. Operation

■ Mounting the evaluation MCU

To mount the evaluation MCU on the adapter board, stand the mounting lever of the IC socket for the evaluation MCU mounted on the adapter board up straight, and then match the index mark (▲) on the evaluation MCU with the index mark (△) on the adapter board. Confirm that the evaluation MCU is correctly mounted, and then move the mounting lever down until it clicks into place. The evaluation MCU is then mounted. (See Figure 3-1.)

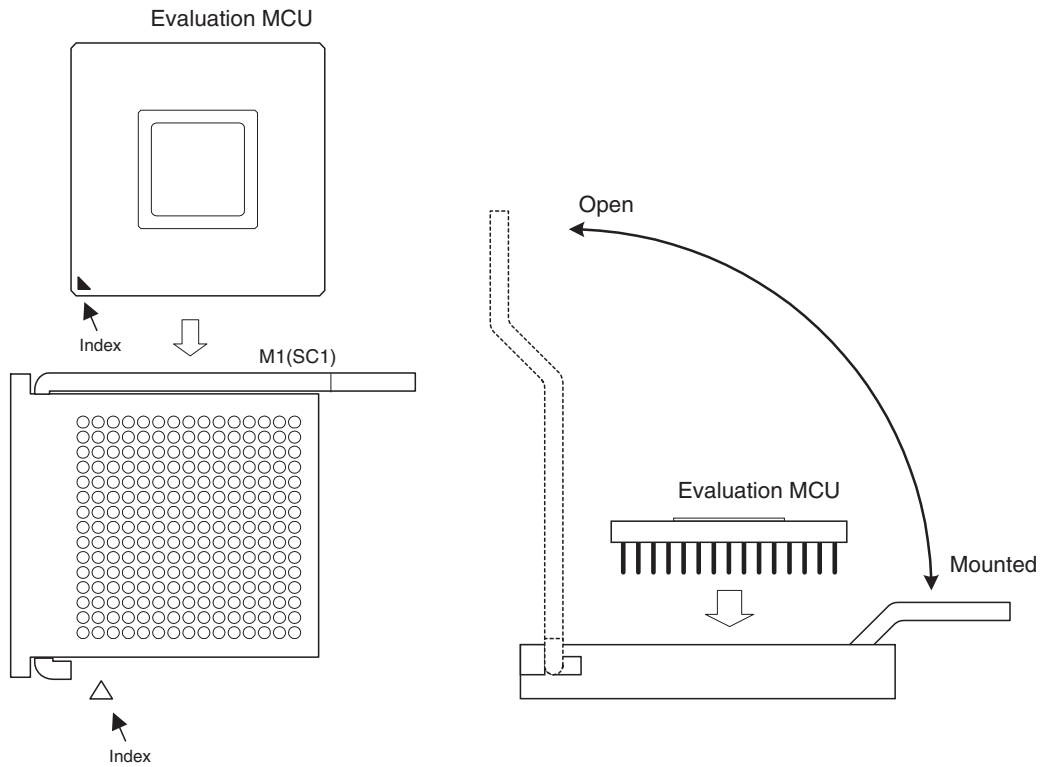


Figure 3-1 Mounting the evaluation MCU

■ Mounting the oscillator

The adapter board is equipped with an IC socket for mounting the crystal oscillator to supply clock pulses to the evaluation MCU.

Figure 3-2 shows how to mount the crystal oscillator in the IC socket. Figure 3-3 shows the clock circuit layout.

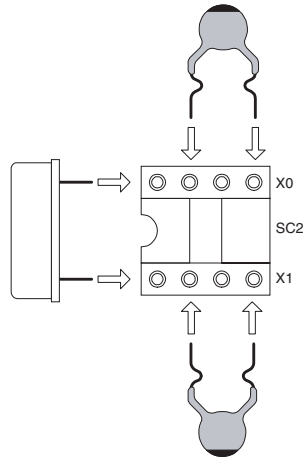


Figure 3-2 Mounting the crystal oscillator

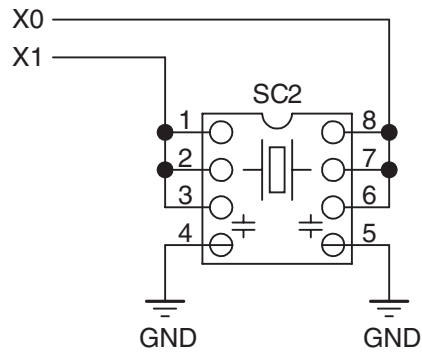


Figure 3-3 Clock circuit

Note:

For the capacitor ratings, see the data sheet for the crystal oscillator that is used.

■ Jumper plug settings

The adapter board is equipped with jumper plugs for making system settings. Figure 3-4 shows the jumper plugs. Table 3-1 lists the settings.

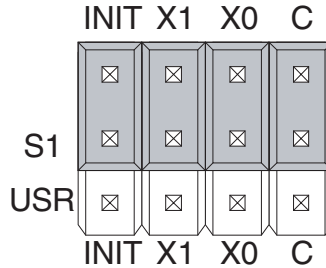


Figure 3-4 Jumper plugs (factory settings)

Table 3-1 Jumper plug settings

Contents	Setting	Function
Setting of capacitor for evaluation MCU internal regulator	USR-C	Set to this position to supply capacitor from the user system
	C	Set to this position to supply capacitor from the adapter board
Setting of clock input source	USR-X0	Set to this position to supply clock pulses from the user system
	X0	Set to this position to supply clock pulses from the adapter board
Setting of clock input destination	USR-X1	Set to this position to supply clock pulses from the user system
	X1	Set to this position to supply clock pulses from the adapter board
Setting of external reset input destination	USR-INT	Set to this position to directly connect the INIT signal from the user system to the evaluation MCU. When only operating the user system that uses the evaluation MCU, set to this position.
	INT	Set to this position to connect the INIT signal from the user system to the emulator. Be sure to set to this position when using the emulator.

4. Adapter Board Dimensions

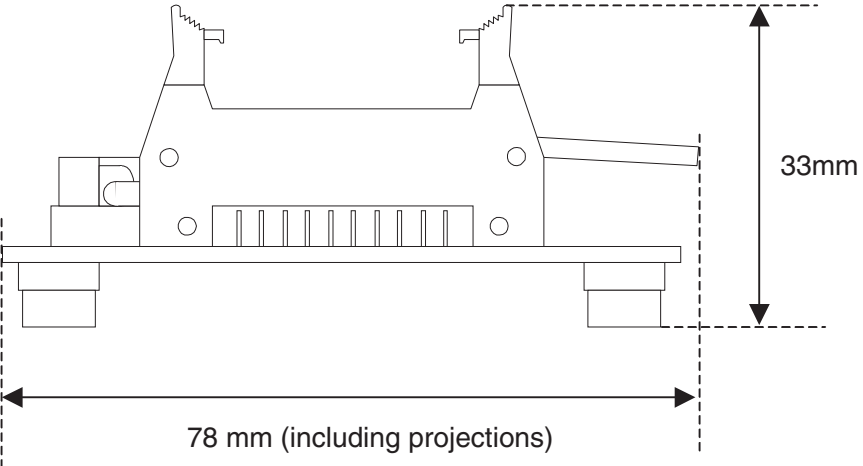
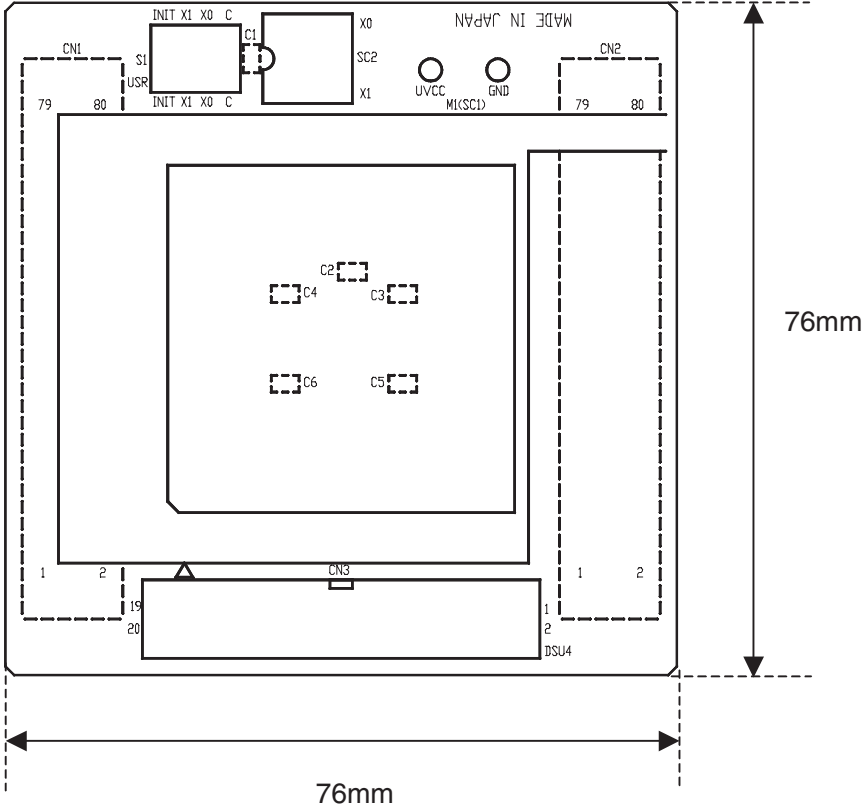


Figure 4-1 Adapter board dimensions

SS01-71012-1E

FUJITSU SEMICONDUCTOR • SUPPORT SYSTEM

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March 2002 the first edition

Published **FUJITSU LIMITED** Electronic Devices

Edited Technical Information Dept.

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