

32-BIT MICROCONTROLLER **MB91460 SERIES**

MB91F467B – CHANGE OF THE BUS
INTERFACE
2007-10-05

Revision History

Date	Issue
2007-10-05	V1.0 First Release

This document contains 8 pages.

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Fujitsu does not bear any warranty in the case this handling note is not fully observed.

1 Devices concerned

The following MB91460 MCU has been changed as described below:

- MB91F467BA

The change has been implemented for MB91F467BA MCUs starting with date code 0731-Z01. The Chip ID for the new variant is: 0x43000041

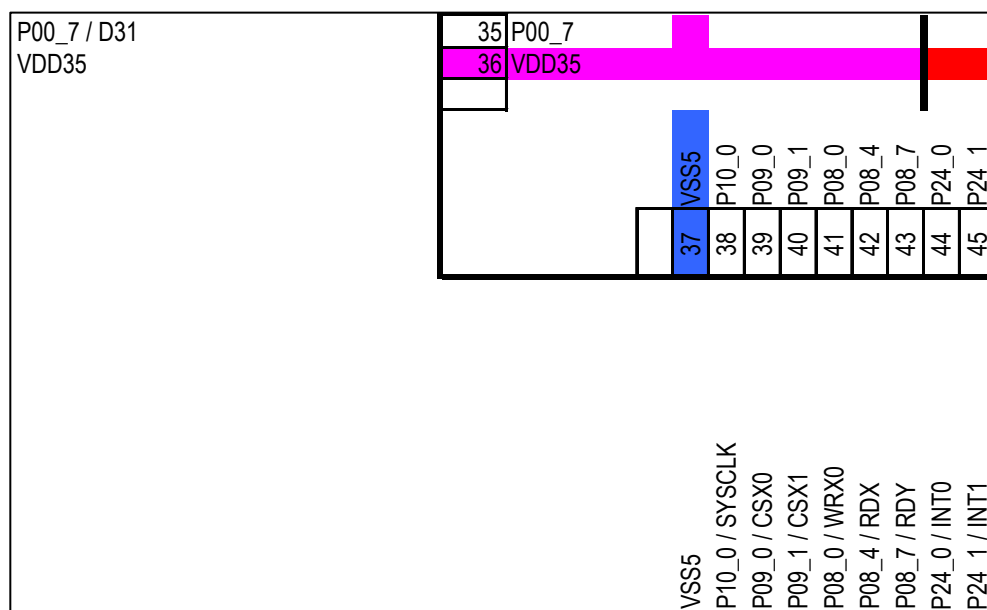
2 Description

To allow an easier connection of external devices to the external bus interface of MB91F467B a second write signal WRX1 is now available if MB91F467B is used in external bus mode (MD3 = 1)

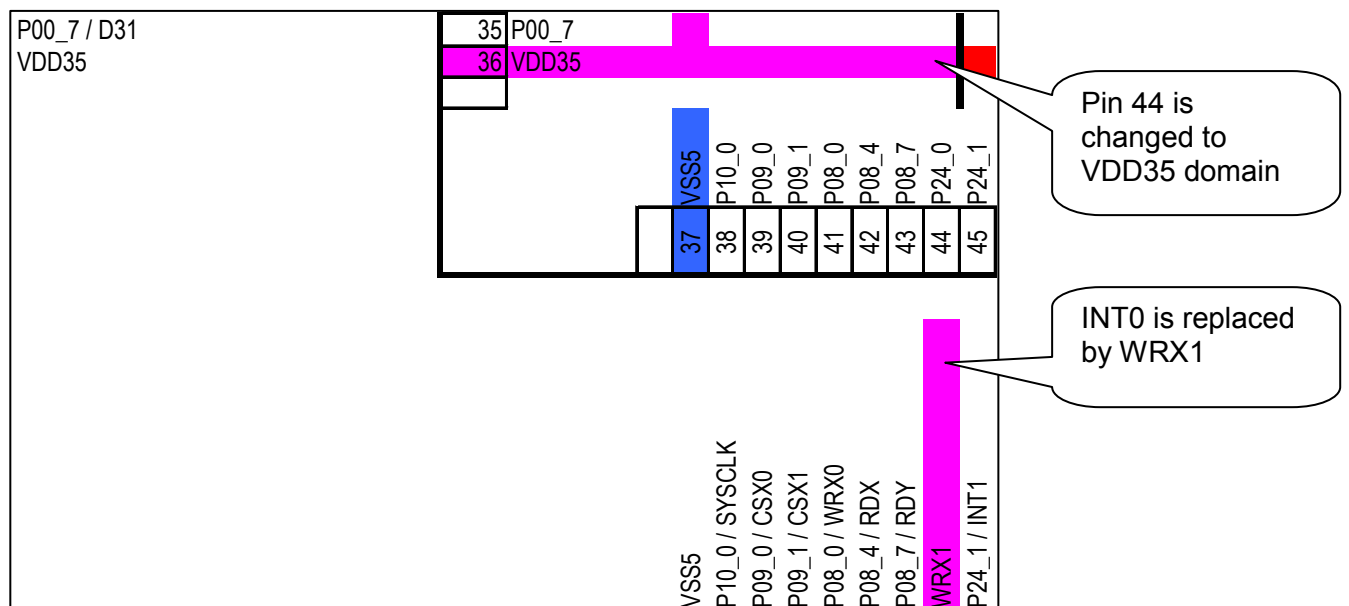
(see also the datasheet of MB91F467B)

For details of this change, please see the diagrams below. They are showing the original and the changed pinning first in external bus mode (MD3 = 1) and then in peripheral mode (MD3 = 0).

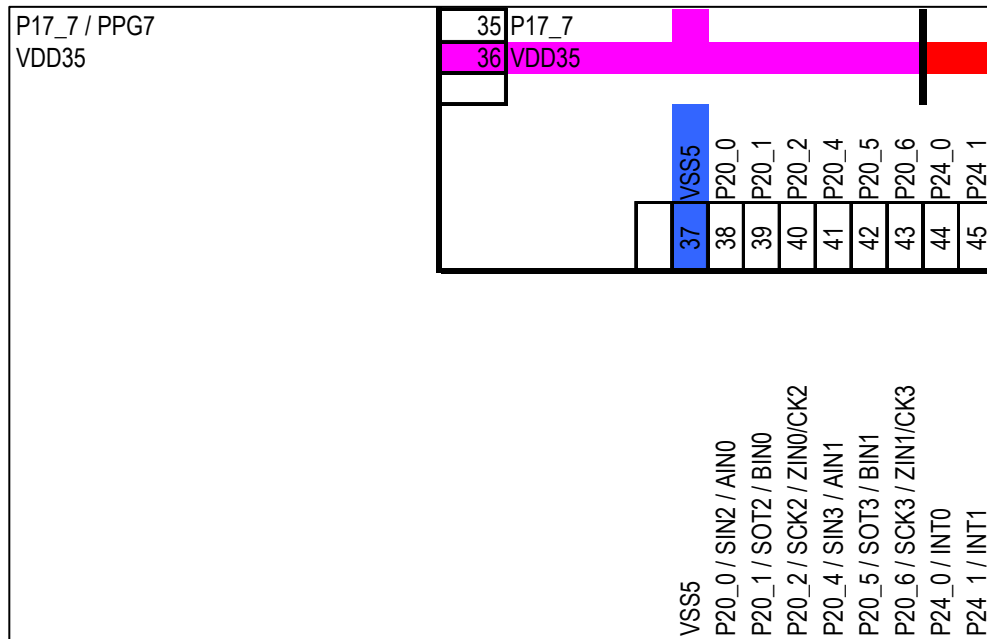
External Bus Mode (MD3=1) – MB91F467BA, date code before 0731-Z01.



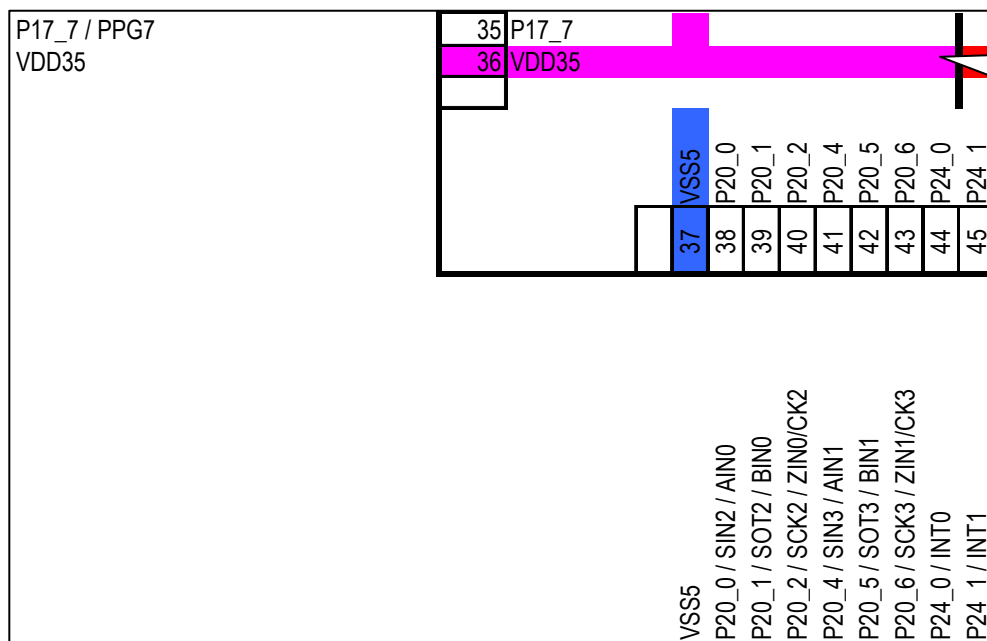
External Bus Mode (MD3= 1) – MB91F467BA, date code from 0731-Z01



Peripheral Mode (MD3 =1) – MB91F467BA, date code before 0731-Z01



Peripheral Mode (MD3 = 1) – MB91F467BA, date code from 0731-Z01



Pin 44 is changed to VDD35 domain

3 References

MB91F467B datasheet

Webpage: http://mcu.emea.fujitsu.com/mcu_product/detail/MB91F467BAPMC.htm